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Weimin Li et alFILING DATE  
January 20, 1999GROUP  
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## U.S. PATENT DOCUMENTS

Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
DV	AA	4,775,478	07/1988	Abernathy et al			
	AB	4,870,470	09/1989	Bass et al			
	AC	5,312,768	05/1994	Gonzalez			
	AD	5,439,838	08/1995	Yang			
	AE	6,001,747	12/1999	Annappagada			
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	AK						
	AL						

## FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	AM							
	AN							
	AO							
	AP							
	AQ							

## OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)

DV	AR		Wolf, S., "Silicon Processing for the VLSI Era," Vol. 1, pps. 189-190.
	AS		
	AT		

EXAMINER

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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.